

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Dae-Woong KANG et al.

Serial No.: 10/758,802 Examiner: Warren, Matthew E.

Filed: January 15, 2004 Group Art Unit: 2815

Confirmation No.: 6500

For: METHODS OF FABRICATING A SEMICONDUCTOR DEVICE HAVING  
MULTI-GATE INSULATION LAYERS AND SEMICONDUCTOR  
DEVICES FABRICATED THEREBY


Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE**

In the Examiner's Statement of Reasons for Allowance of the Office Action dated July 8, 2008, the examiner stated that the prior art of record does not teach some of the elements recited in independent claim 29. Applicants note that, when considering patentability, claims should be considered as whole according to the claim language. Applicants also note it is a well-known tenet of patent law that each allowed patent claim stands alone. The examiner has only stated reasons for allowance of the independent claim. The dependent claims, however, are also independently allowable. Further, although the examiner has indicated some reasons for allowance, there can be other reasons that claims are allowable.

**Customer No. 20575**

Respectfully submitted,  
MARGER JOHNSON & McCOLLOM, P.C.

  
\_\_\_\_\_  
Kurt Eaton, Reg. No. 51,640

MARGER JOHNSON & McCOLLOM, P.C.  
210 SW Morrison Street, Suite 400  
Portland, OR 97204  
503-222-3613